FIG. 1A

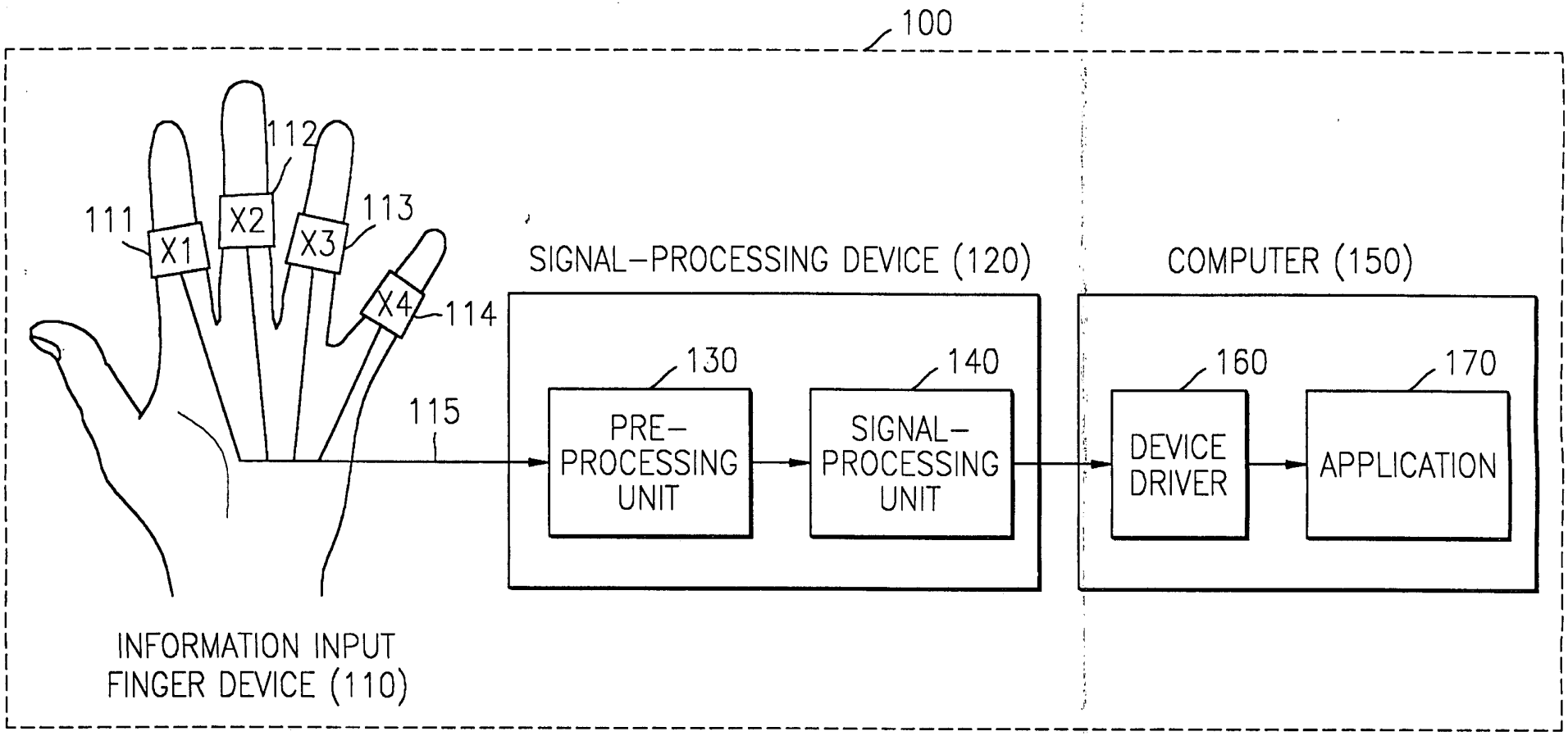

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FIG. 1B

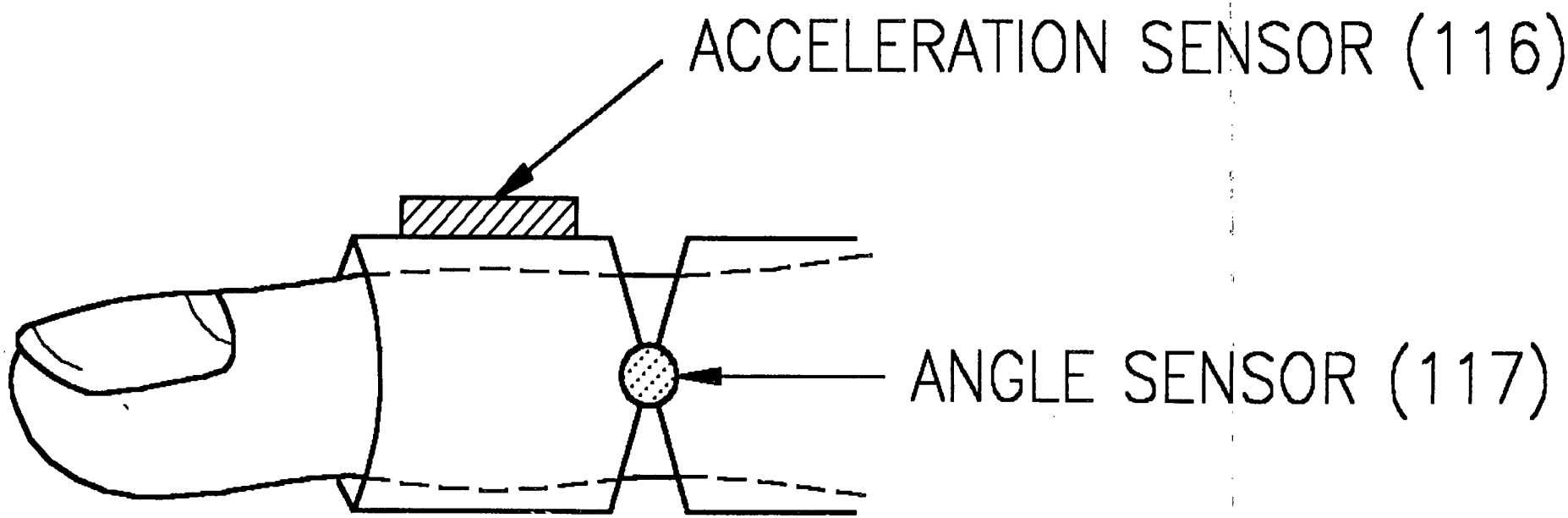

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FIG. 2

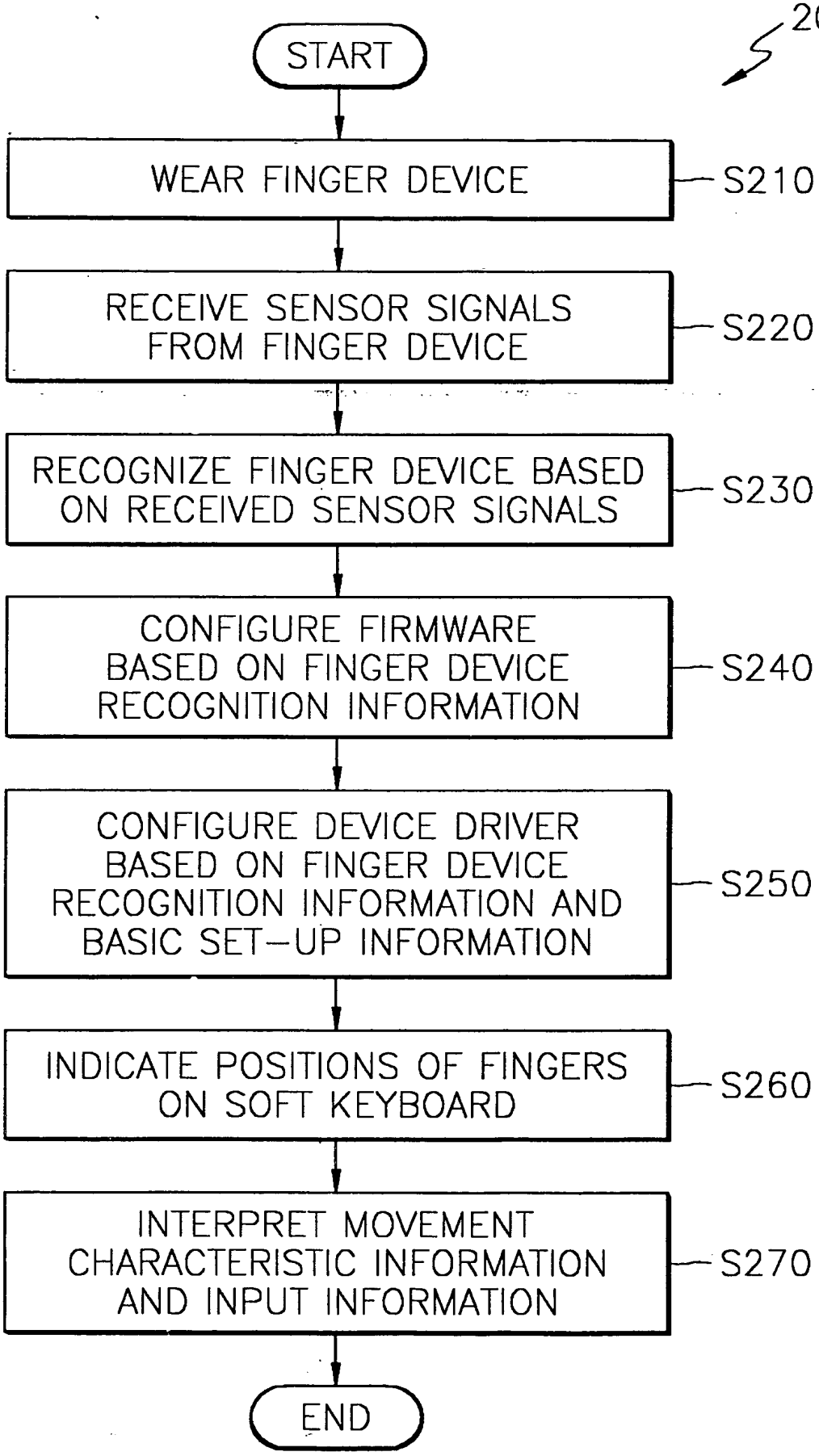

FIG. 3A

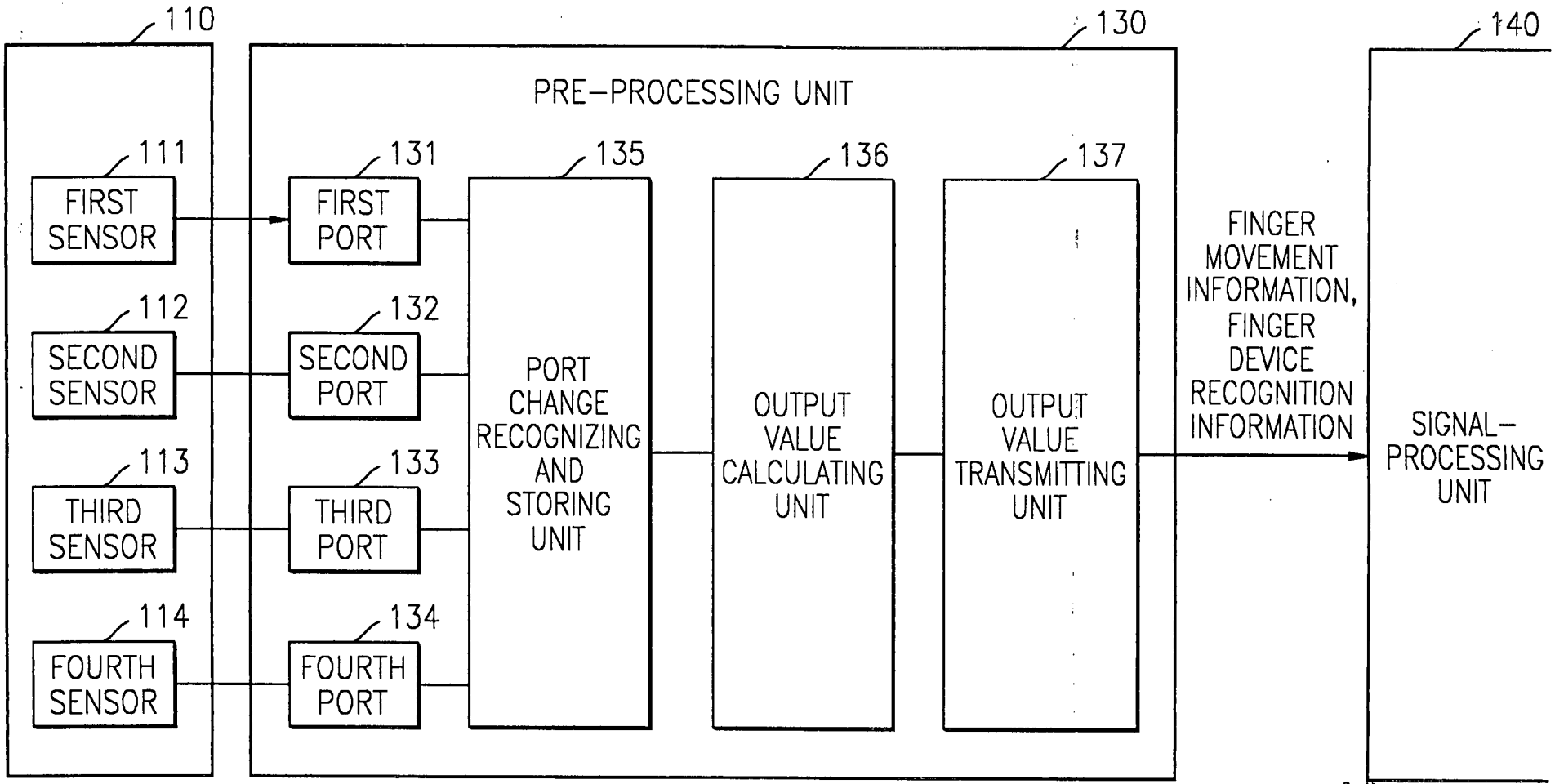

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FIG. 3B

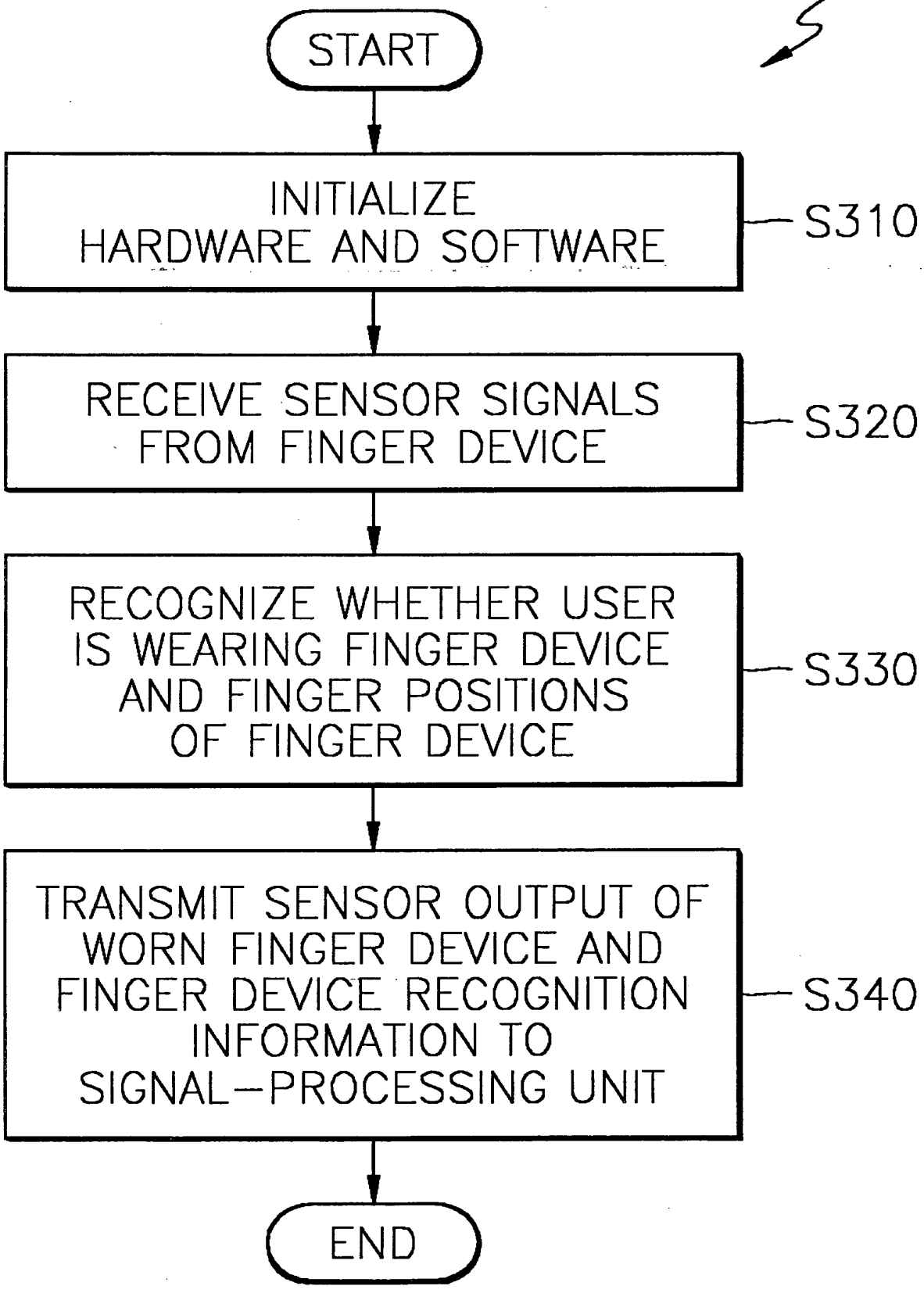

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FIG. 4A

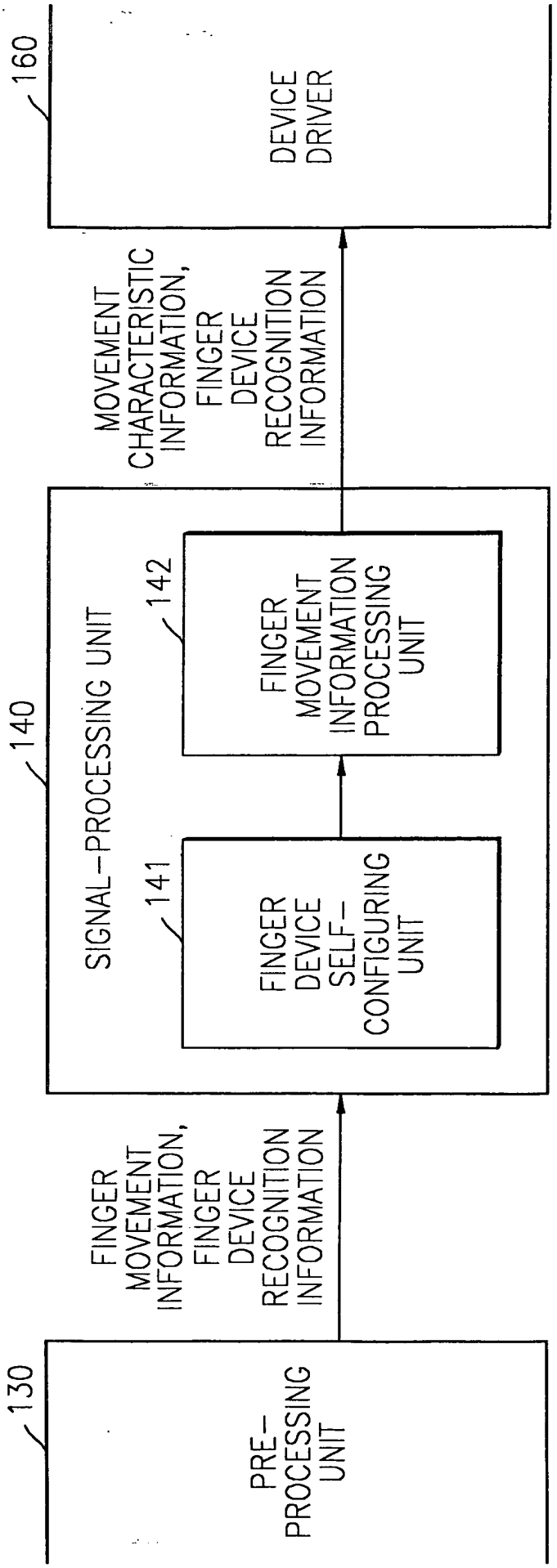

FIG. 4B

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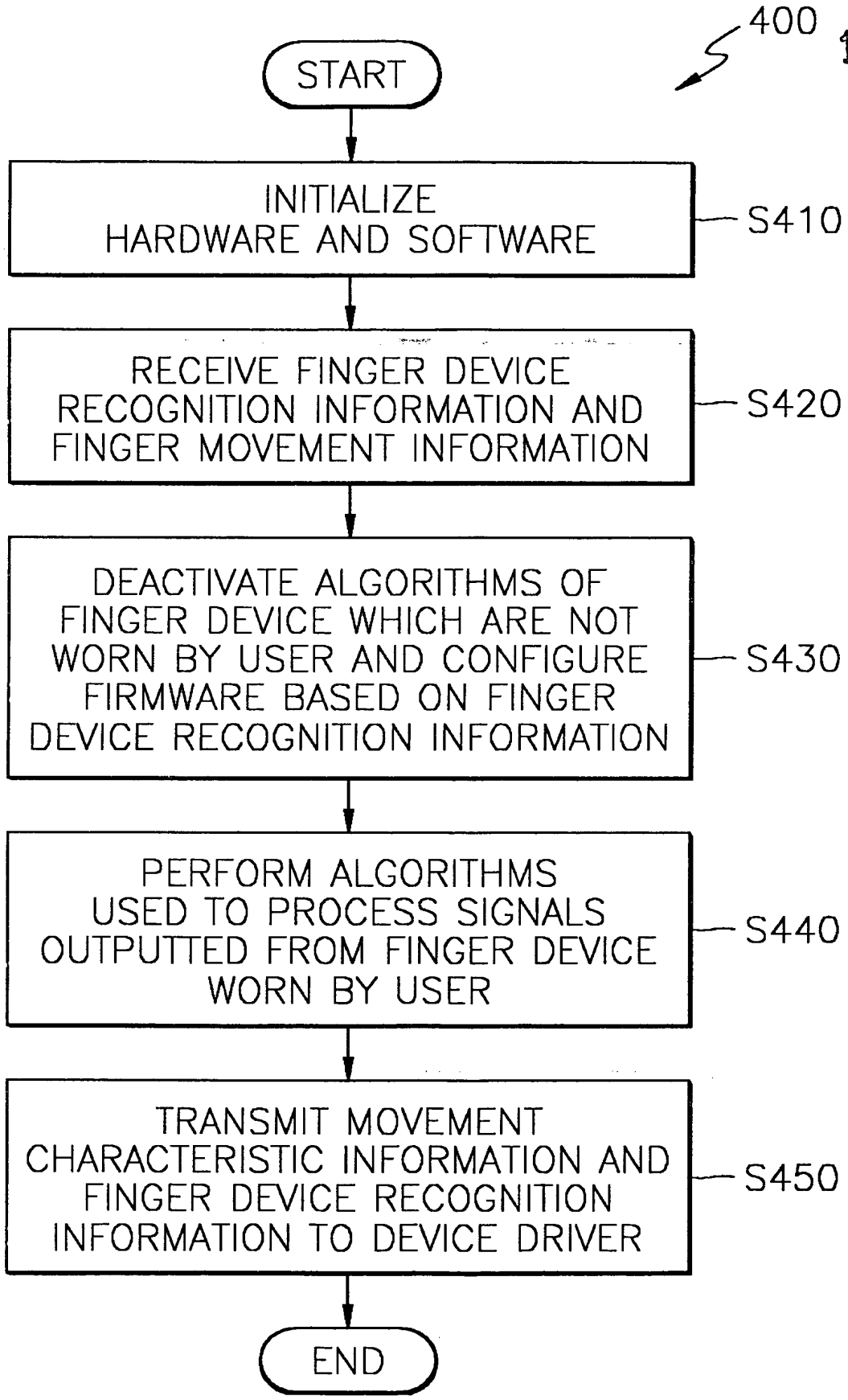

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FIG. 5A

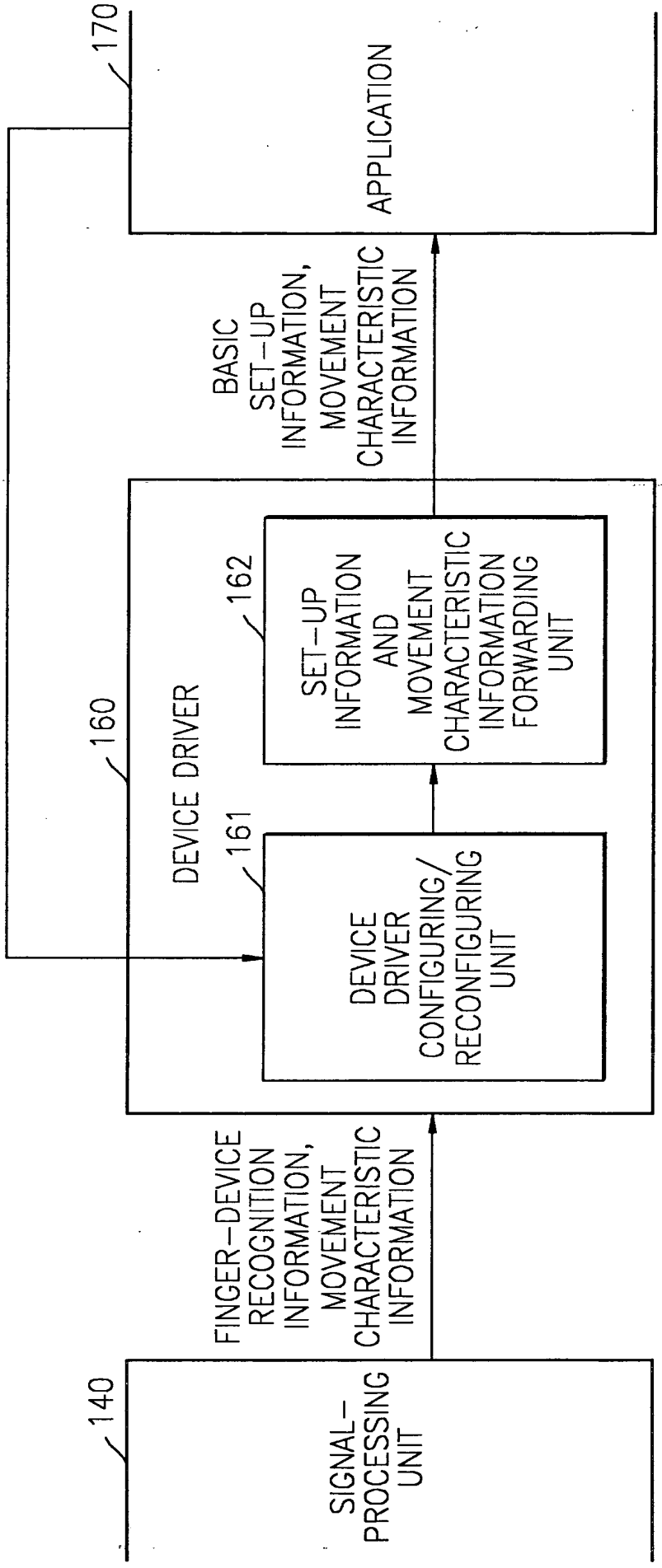

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FIG. 5B

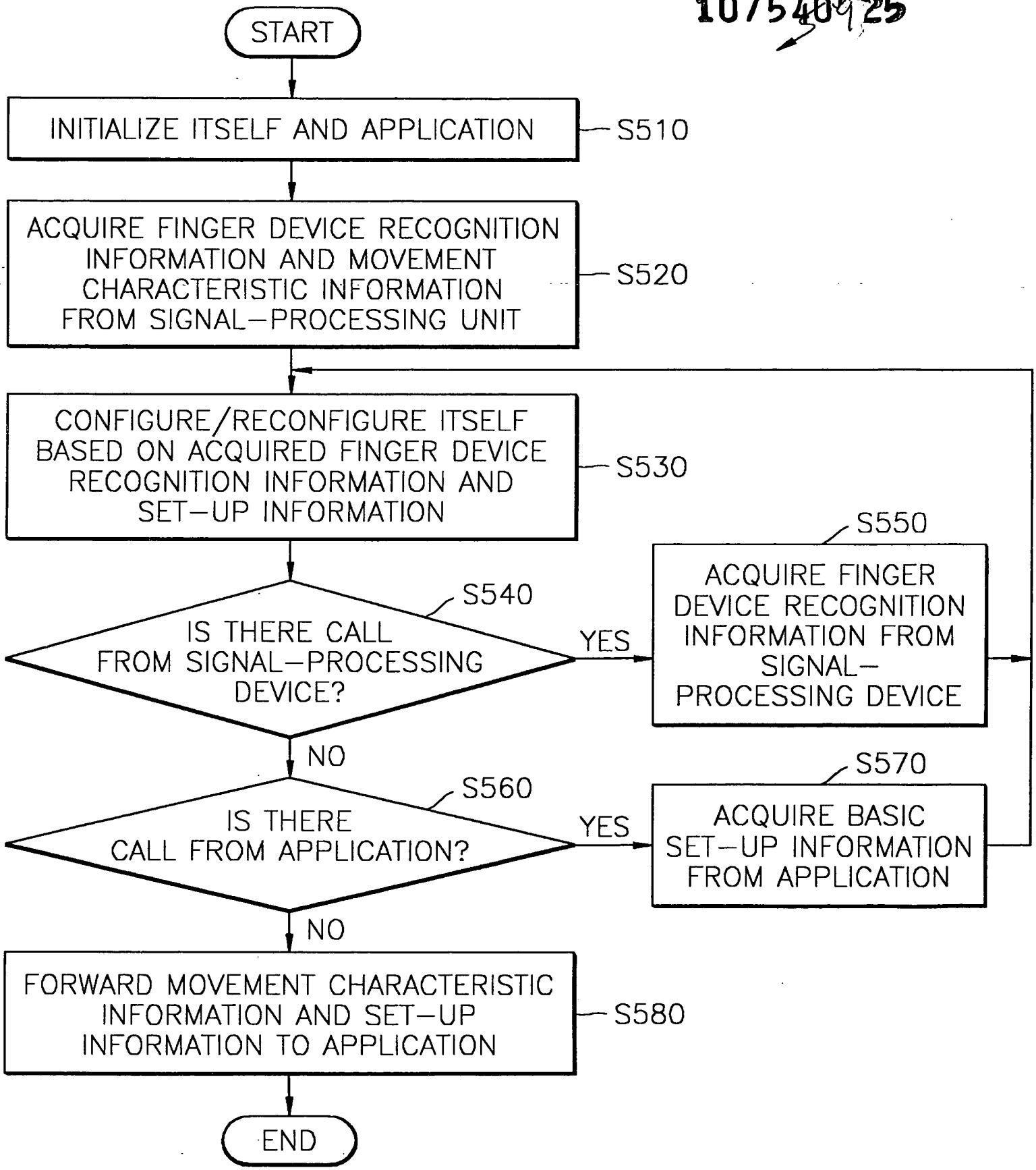

FIG. 6A

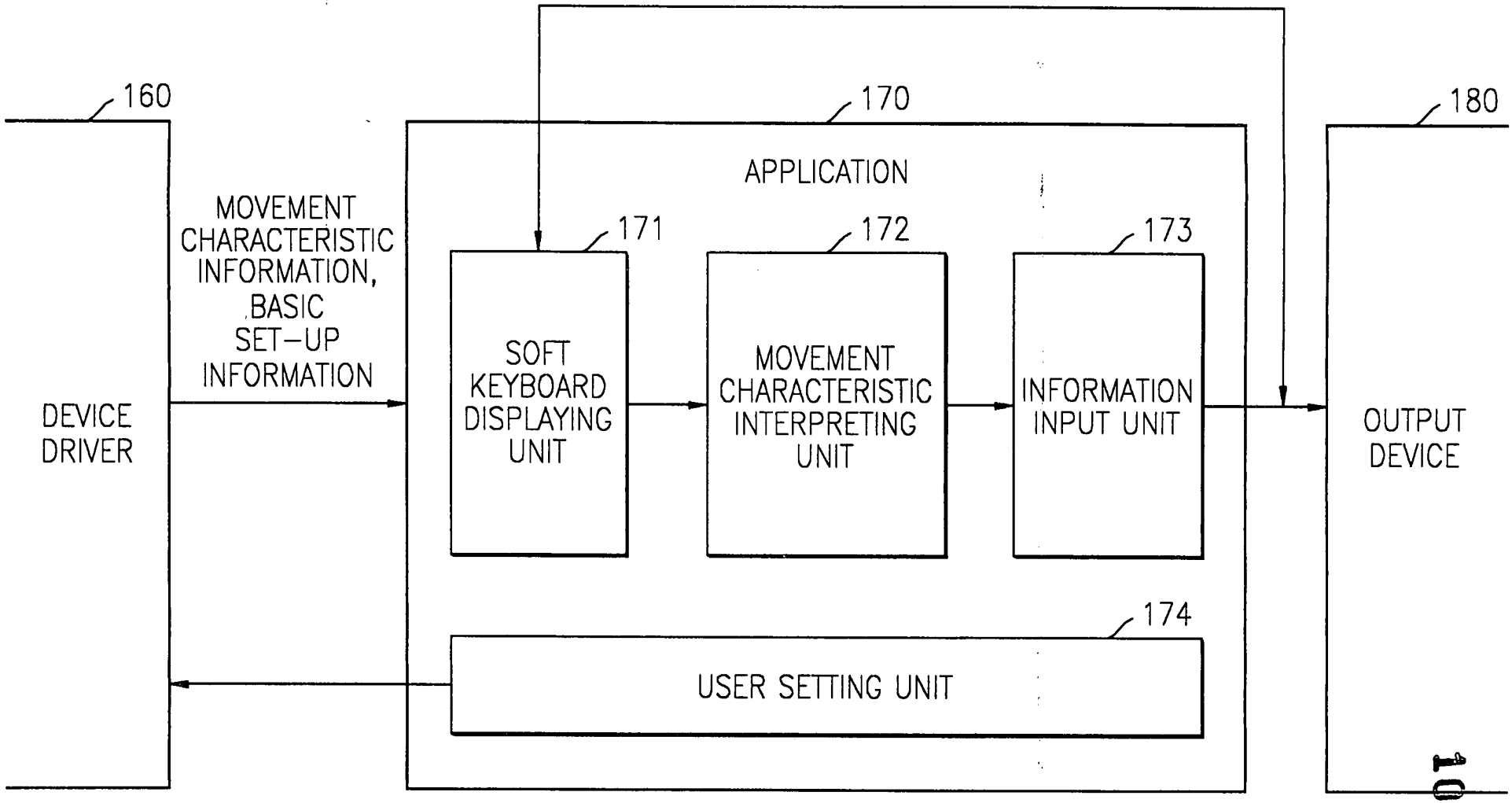

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FIG. 6B

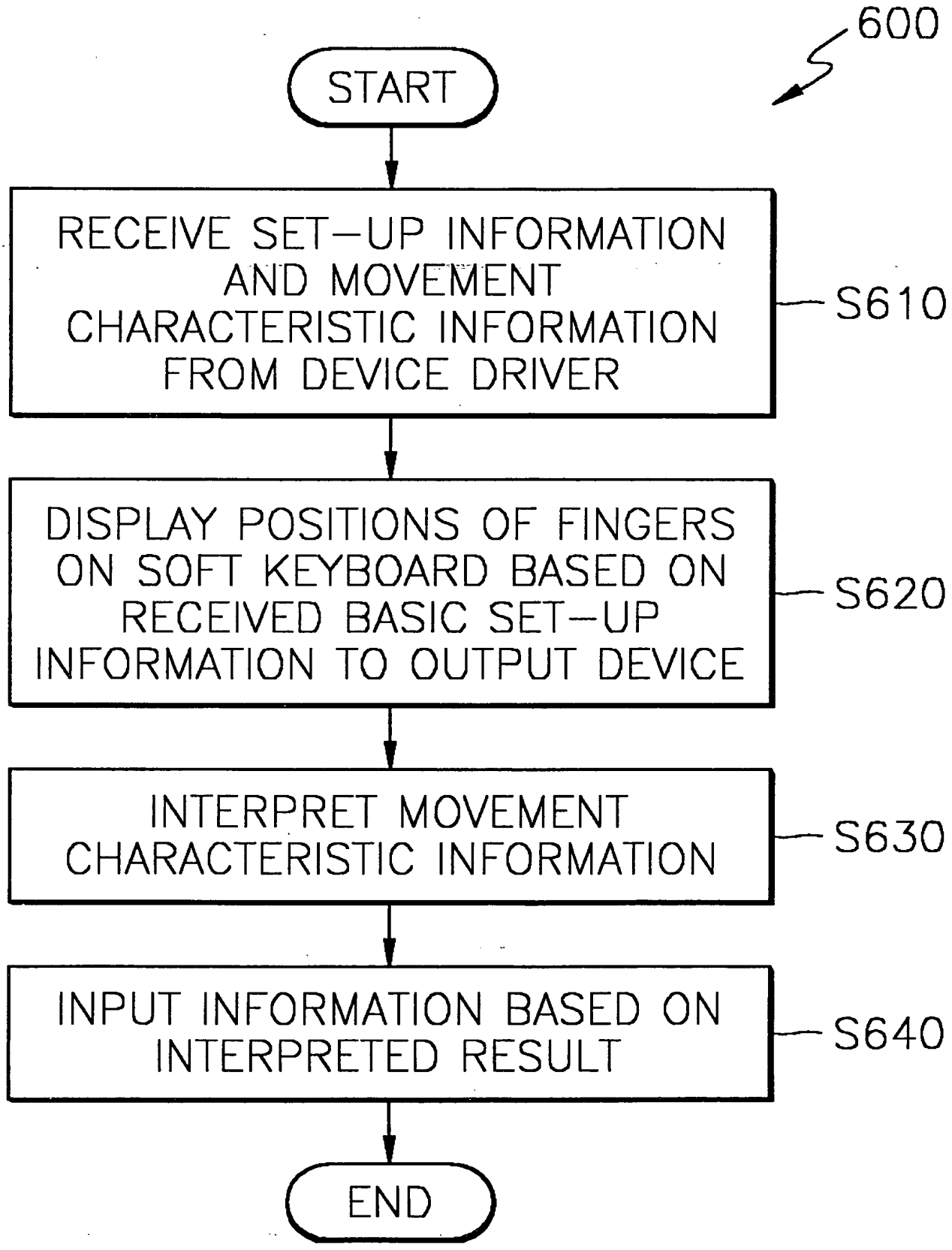

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FIG. 7

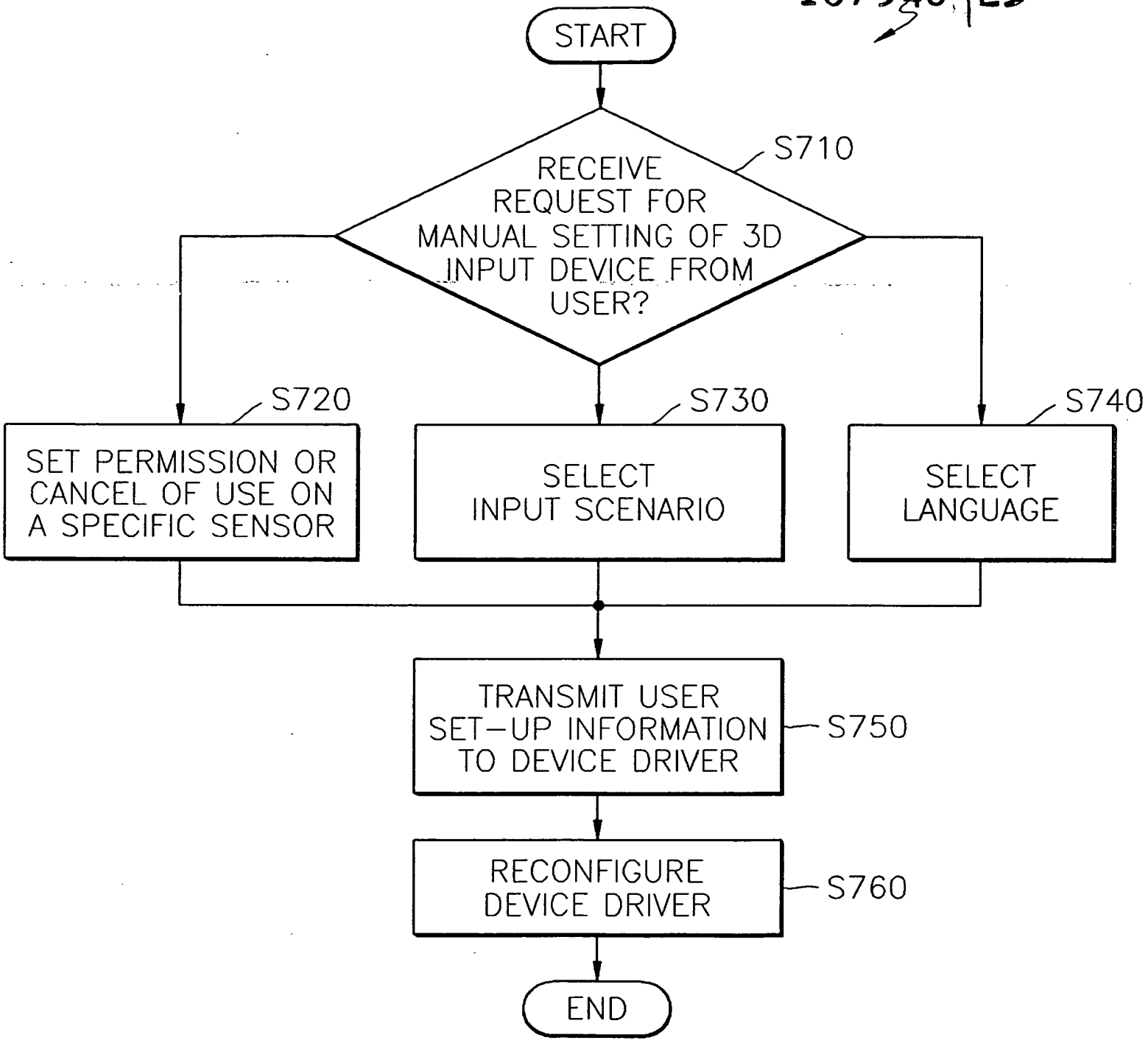

FIG. 8

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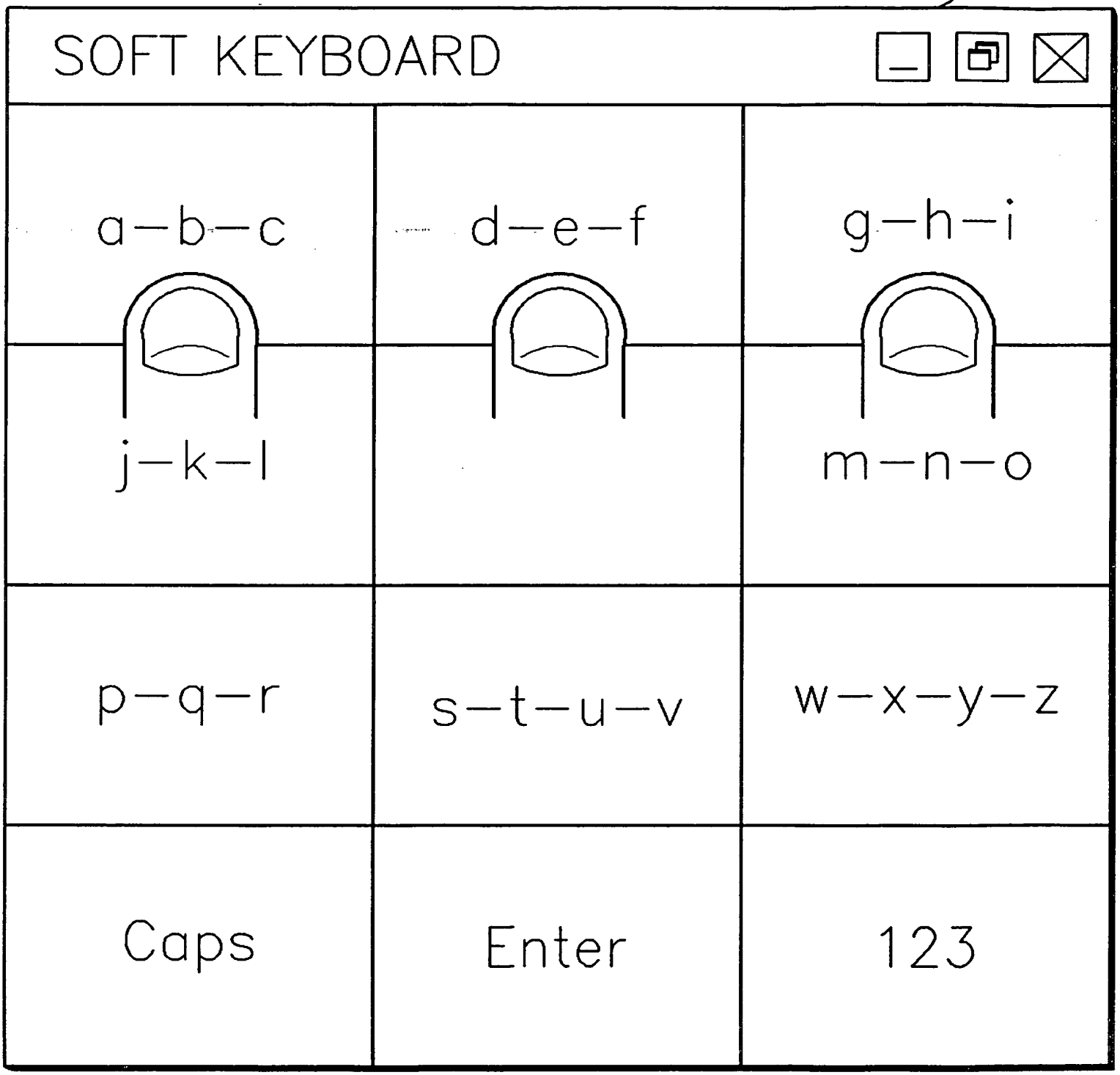

FIG. 9

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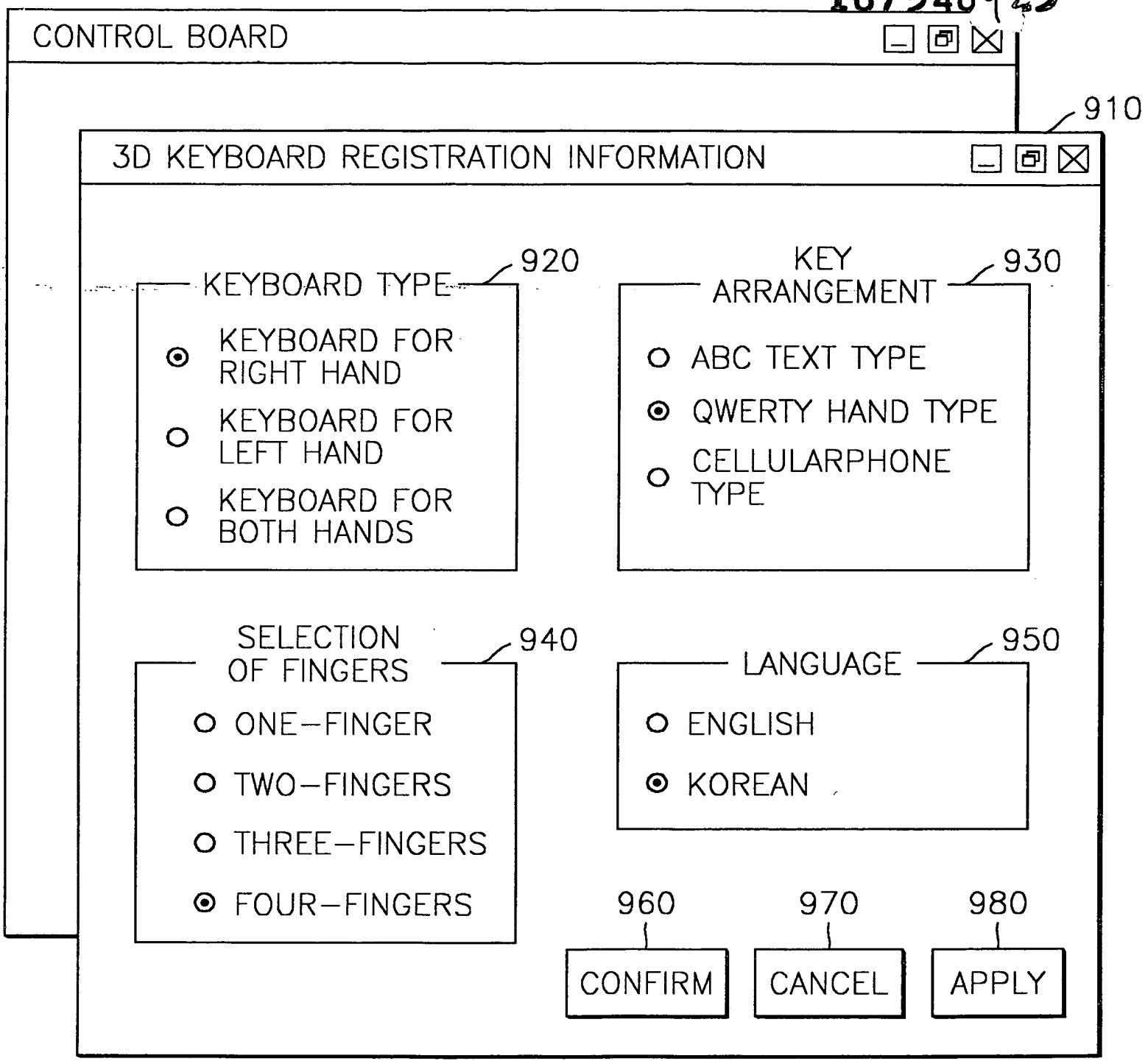

FIG. 10

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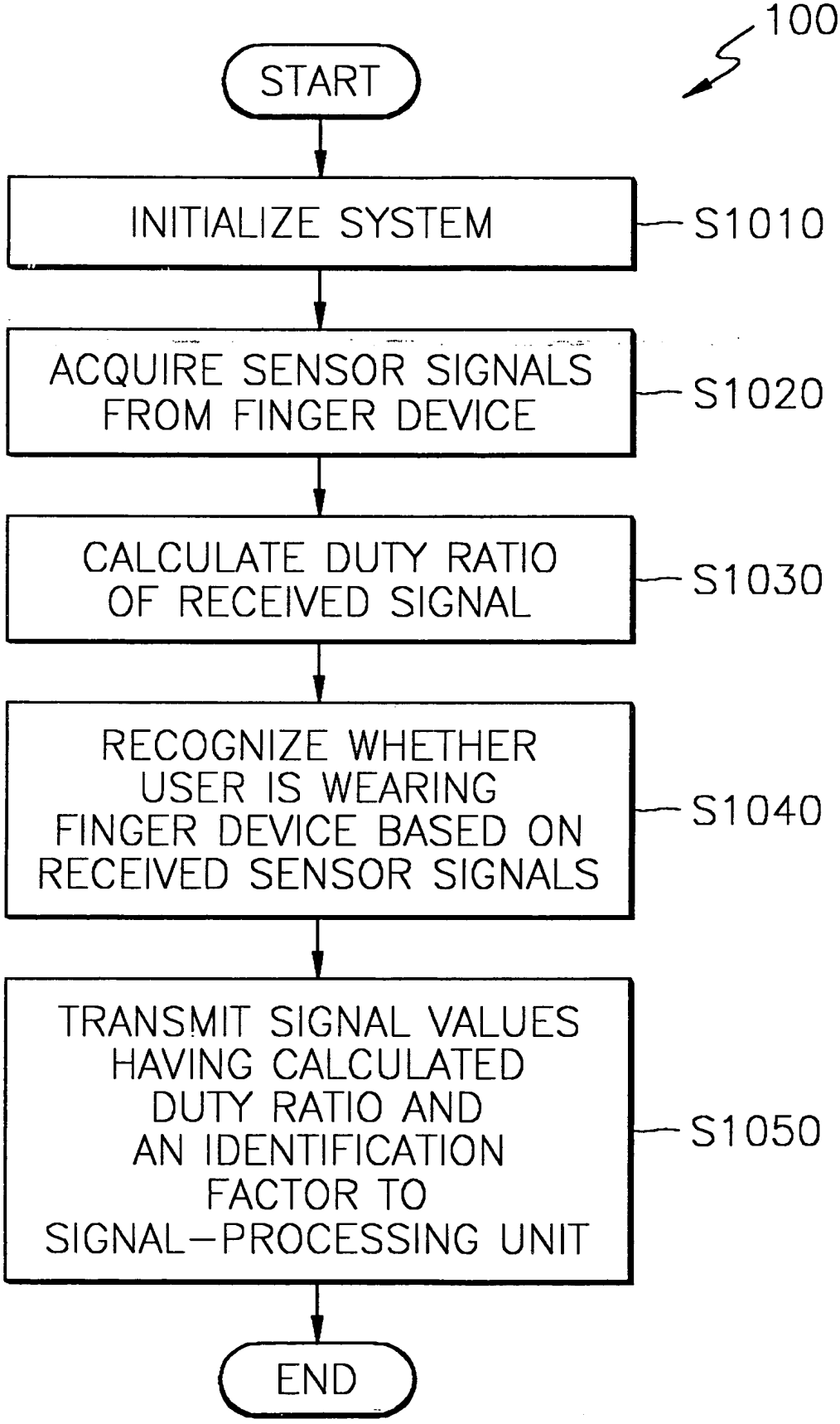

FIG. 11

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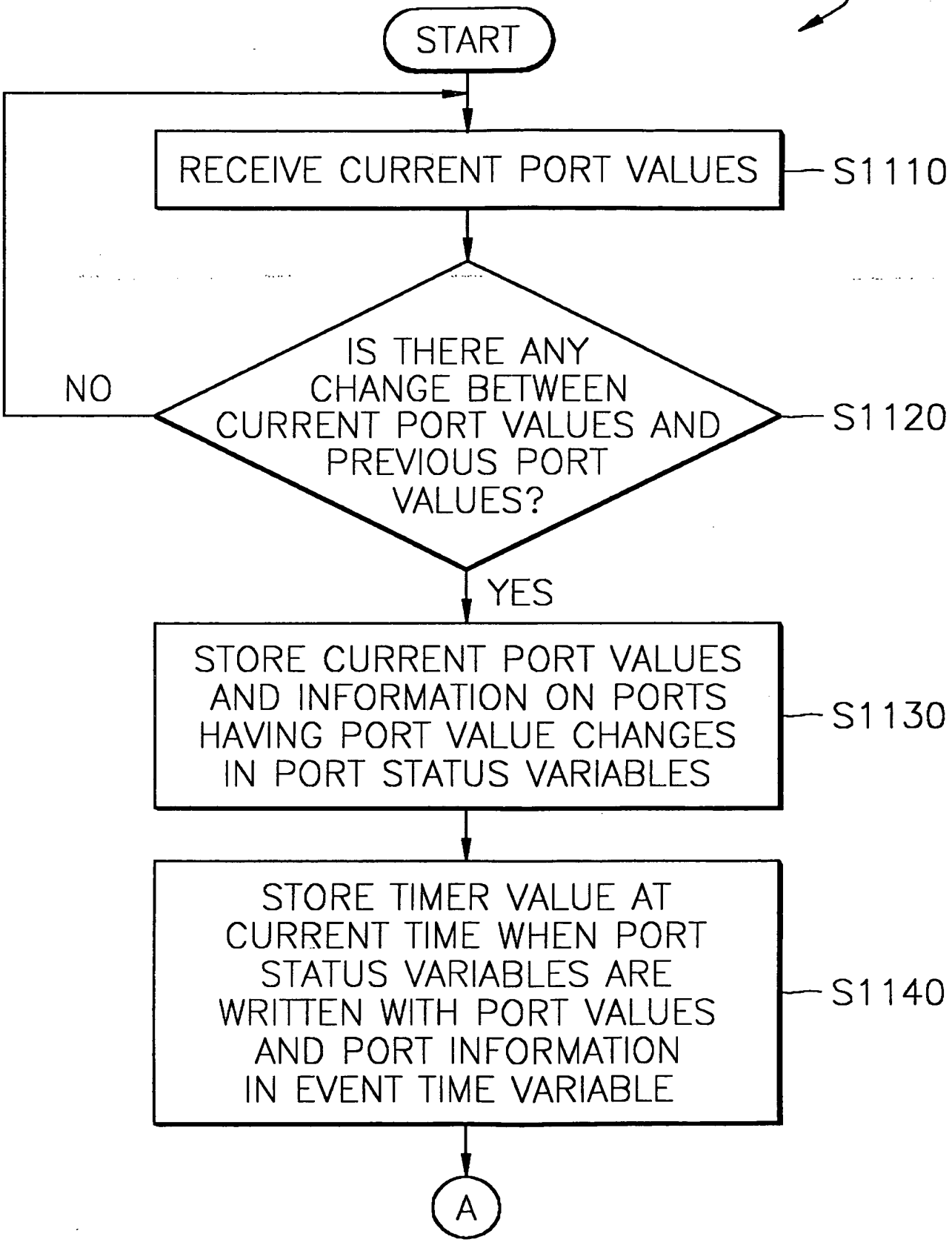
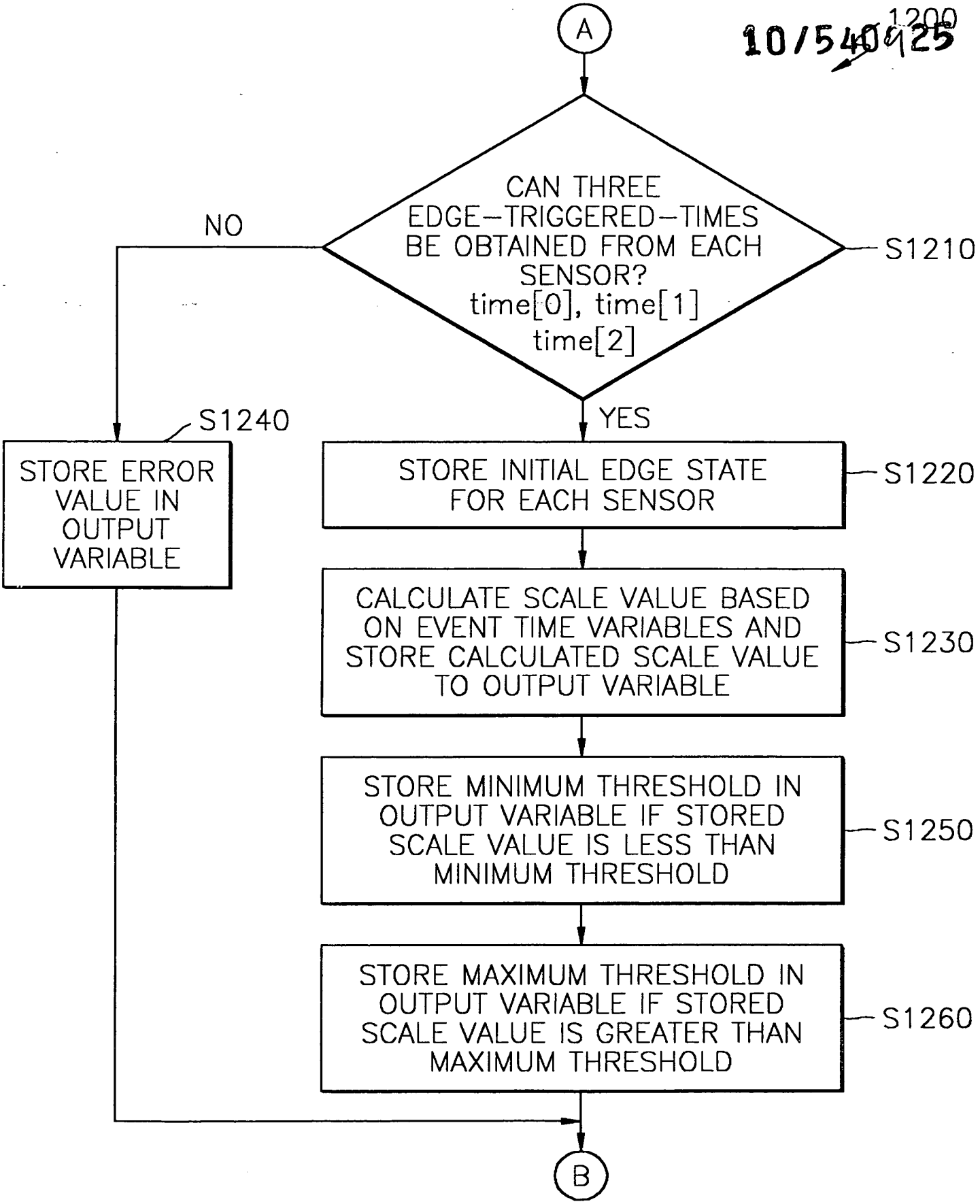


FIG. 12


1200
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A
CAN THREE EDGE-TRIGGERED-TIMES BE OBTAINED FROM EACH SENSOR? time[0], time[1] time[2]
S1210
NO
YES
S1240
STORE ERROR VALUE IN OUTPUT VARIABLE
STORE INITIAL EDGE STATE FOR EACH SENSOR
S1220
CALCULATE SCALE VALUE BASED ON EVENT TIME VARIABLES AND STORE CALCULATED SCALE VALUE TO OUTPUT VARIABLE
S1230
STORE MINIMUM THRESHOLD IN OUTPUT VARIABLE IF STORED SCALE VALUE IS LESS THAN MINIMUM THRESHOLD
S1250
STORE MAXIMUM THRESHOLD IN OUTPUT VARIABLE IF STORED SCALE VALUE IS GREATER THAN MAXIMUM THRESHOLD
S1260
B

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FIG. 13

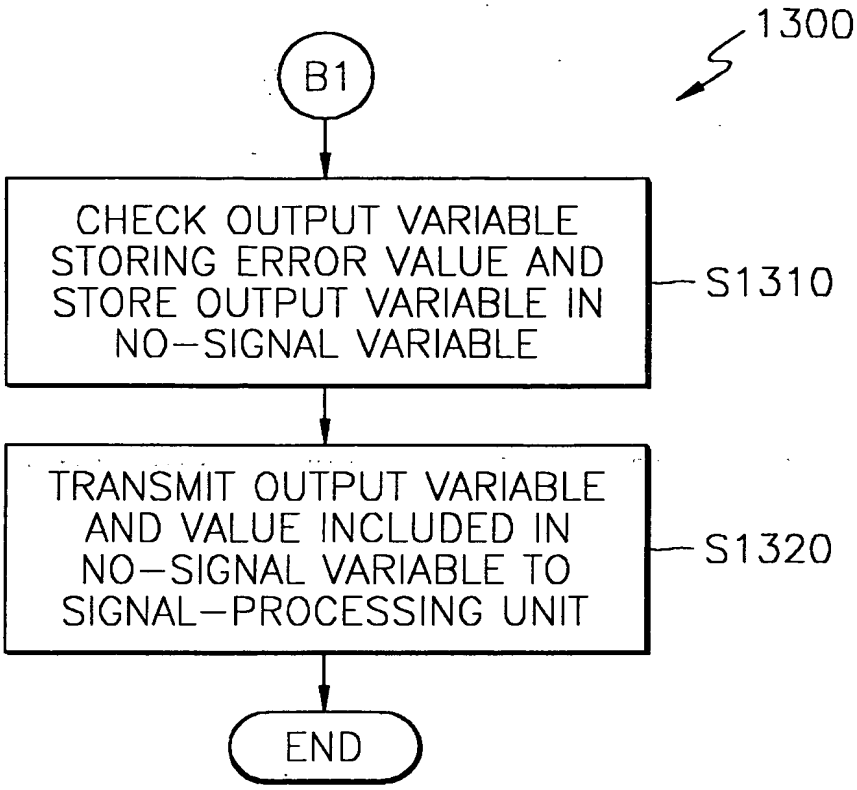

FIG. 14

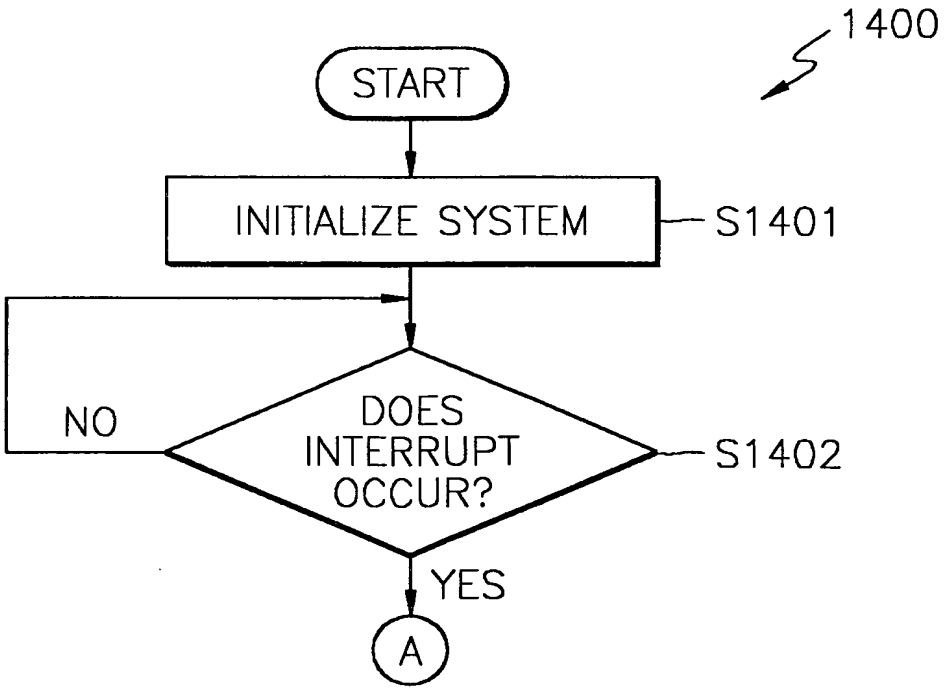

FIG. 15A

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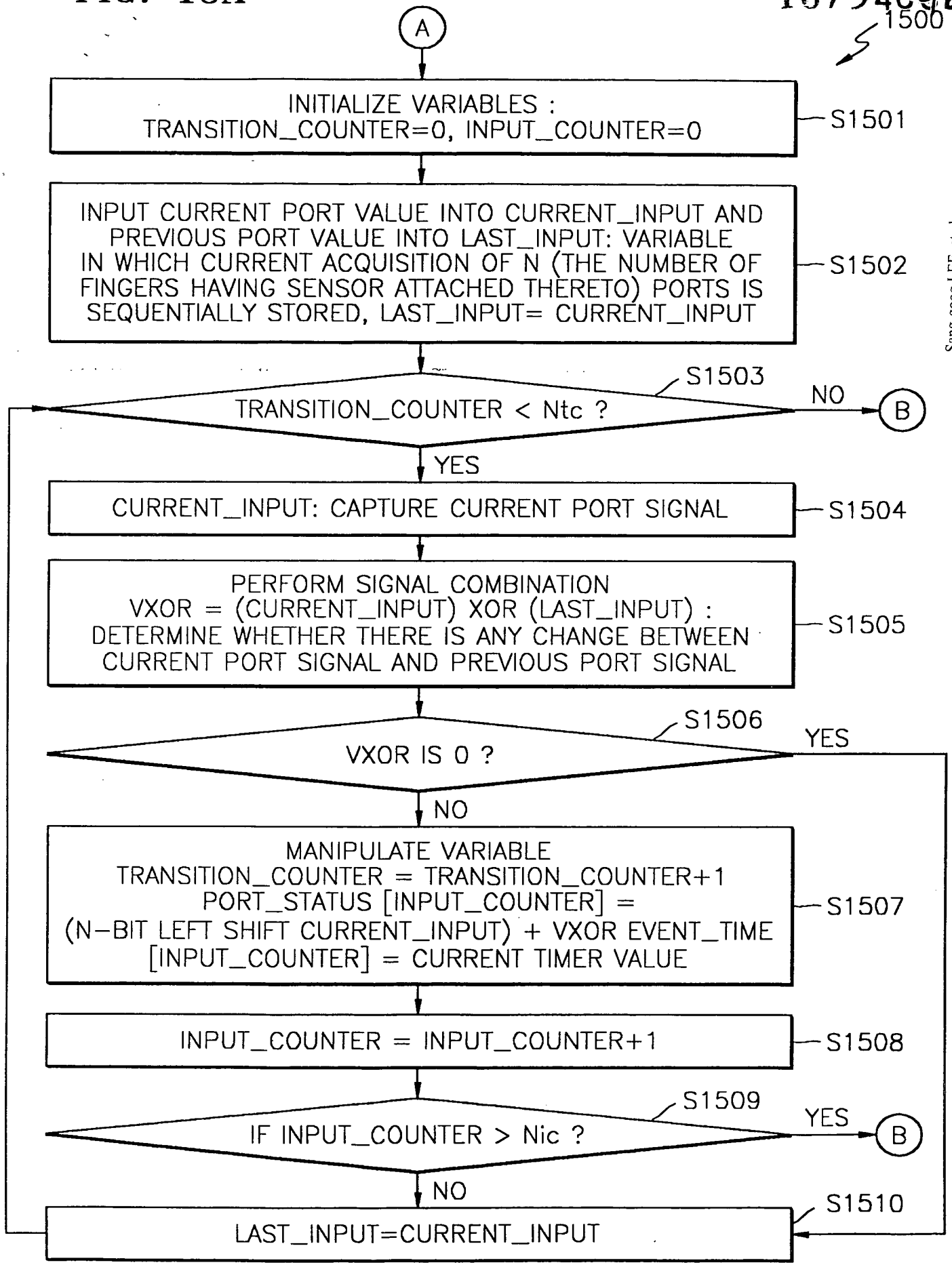

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FIG. 15B

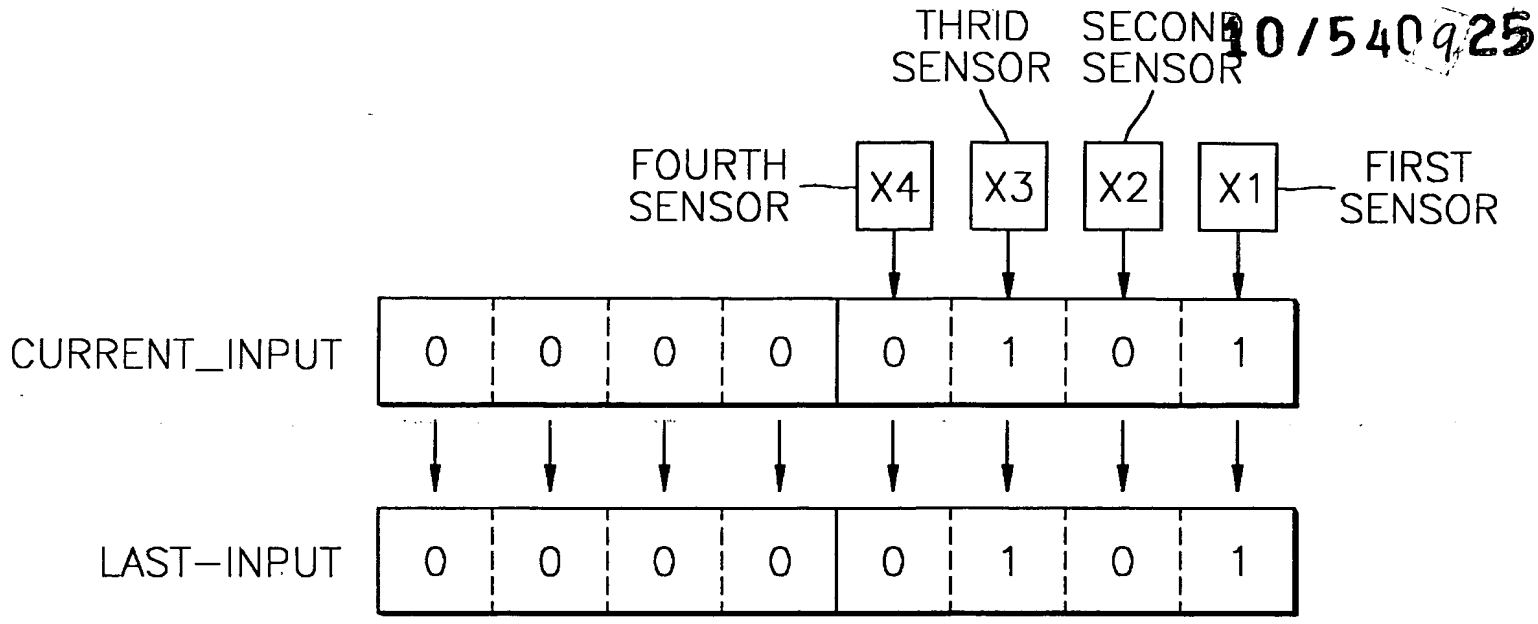

FIG. 15C

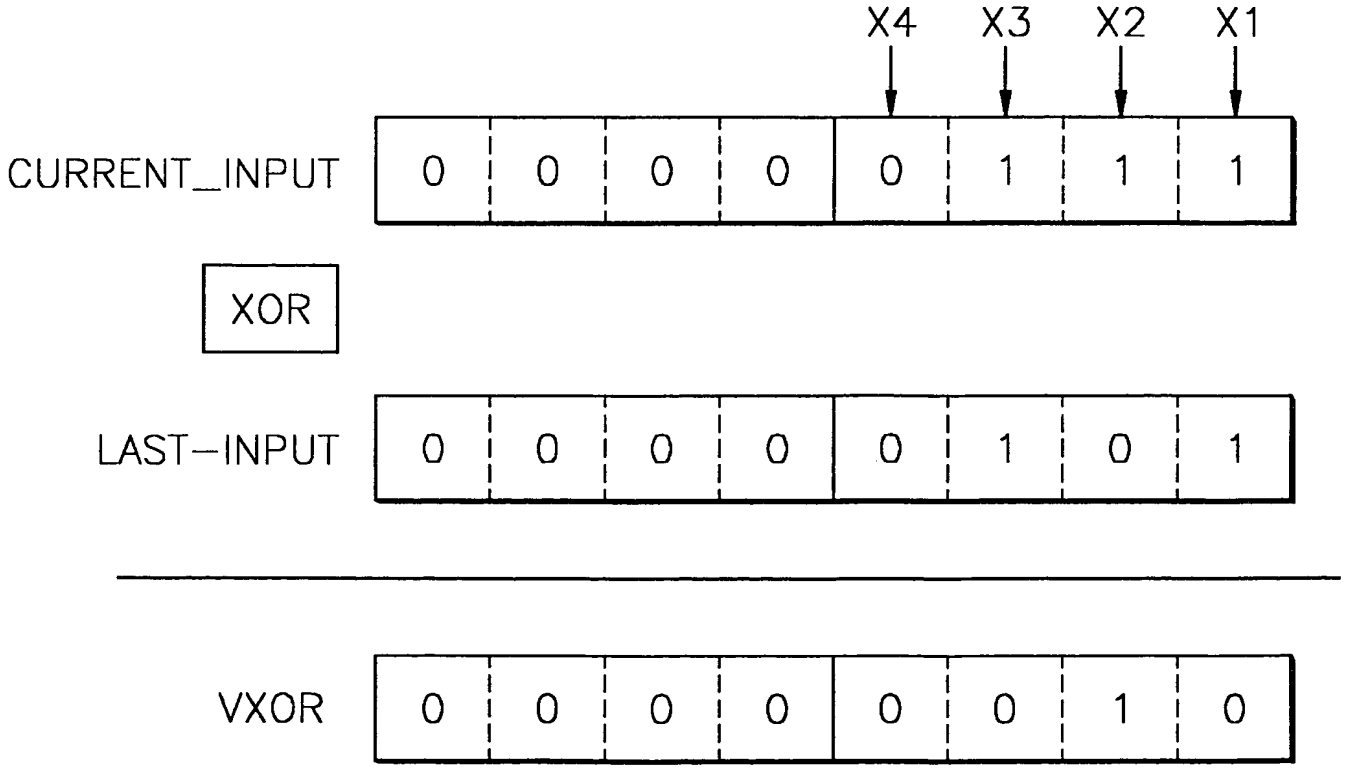

FIG. 15D

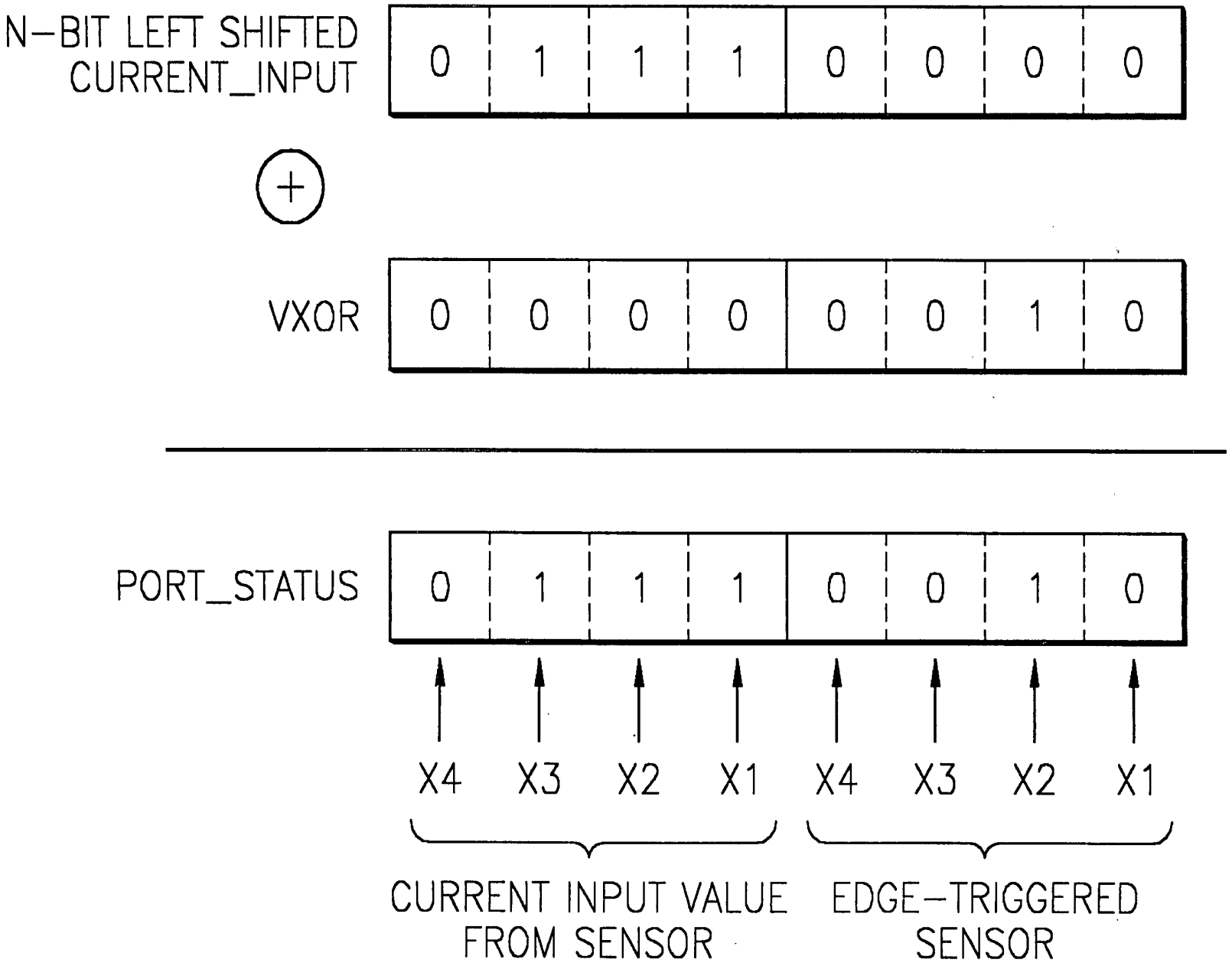

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FIG. 15E

INPUT COUNTER	CURRENT_ INPUT	LAST_INPUT	VXOR	TRANSITION_ COUNTER	PORT_STATUS[ ]	EVENT_TIME[ ]
0	0000 0101					
	0000 0111	0000 0101	0000 0000	1	0111 0010	10
1	0000 1111	0000 0111	0000 1000	2	1111 1000	50
2	0000 1110	0000 1111	0000 0001	3	1110 0001	130
3	0000 1010	0000 1110	0000 0010	4	1010 0100	160
4	0000 1000	0000 1010	0000 1000	5	1000 0010	170
5	0000 0000	0000 1000	0000 1000	6	0000 1000	210
6	0000 0001	0000 0000	0000 0001	7	0001 0001	280
7	0000 0101	0000 0001	0000 0100	8	0101 0100	300
8	0000 0111	0000 0101	0000 0010	9	0111 0010	320
9	0000 1111	0000 0111	0000 1000	10	1111 1000	360
10	0000 1110	0000 1111	0000 0001	11	1110 0001	430
11	0000 1010	0000 1110	0000 0100	12	1010 0100	450

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FIG. 16A

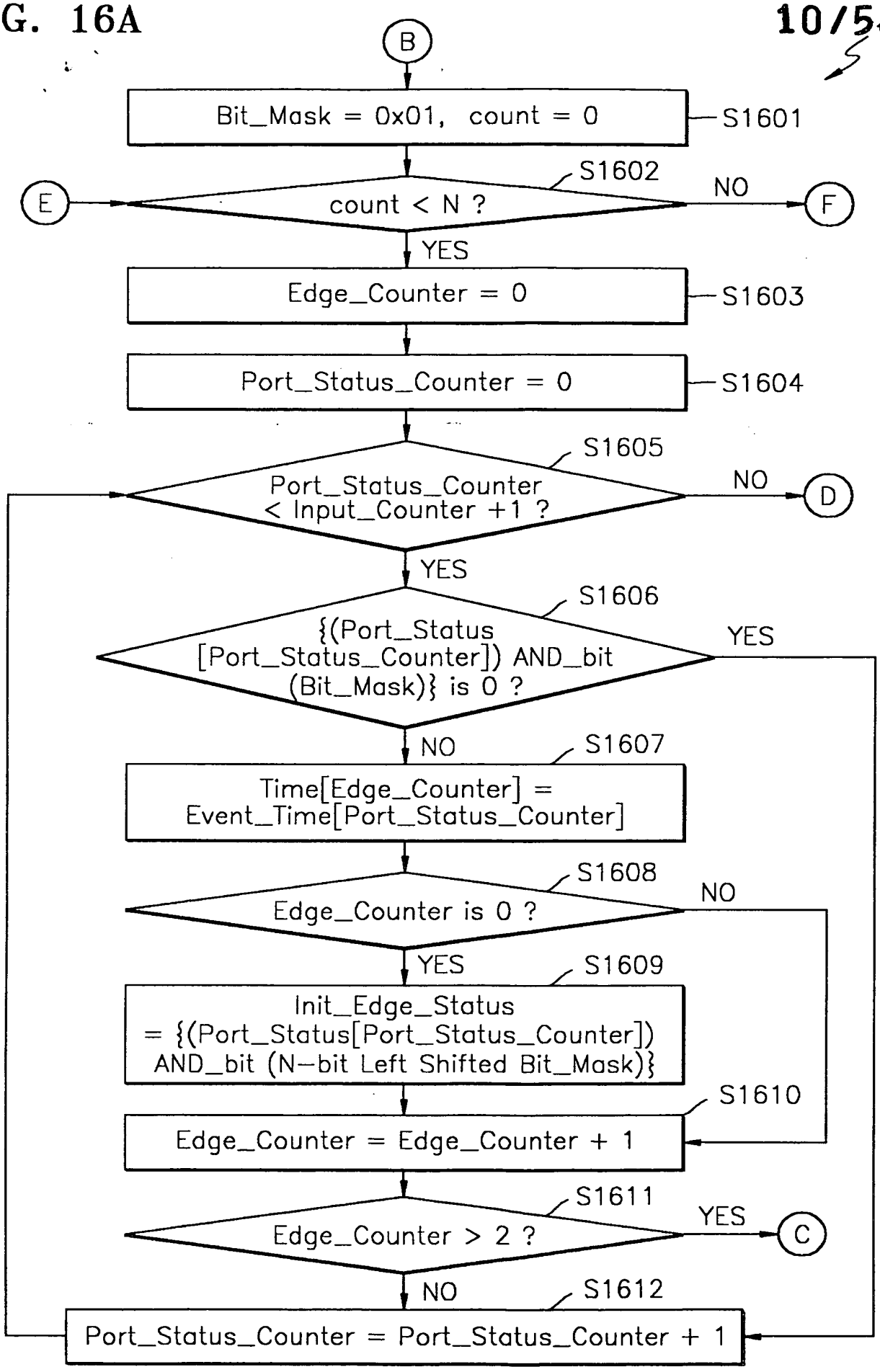

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FIG. 16B

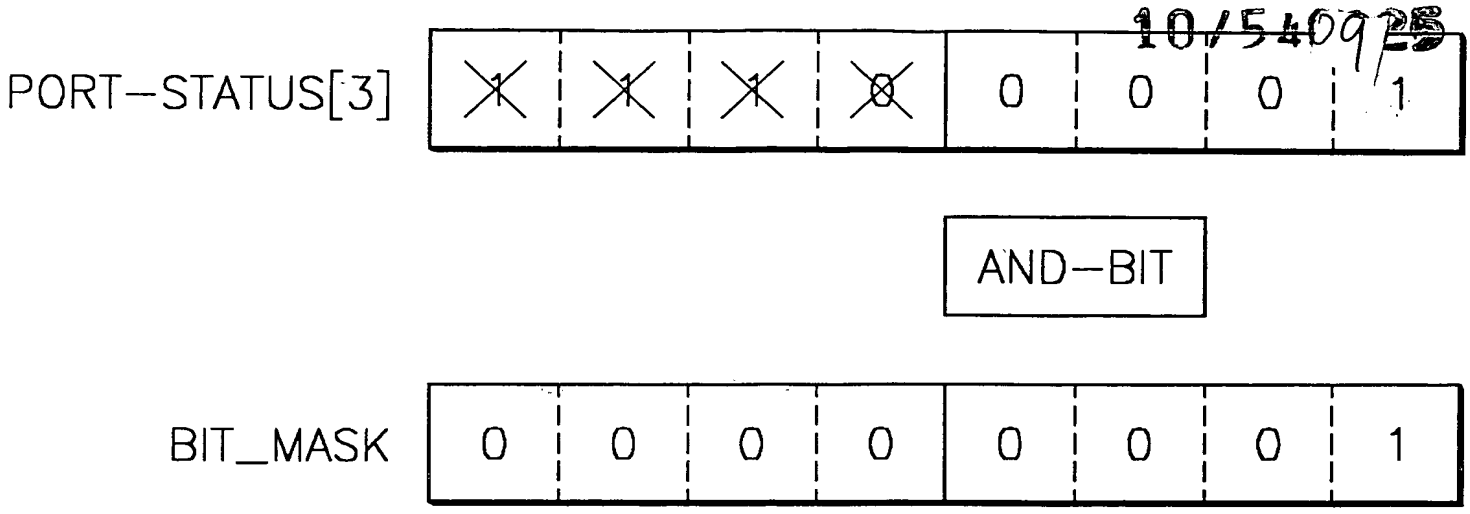

FIG. 16C

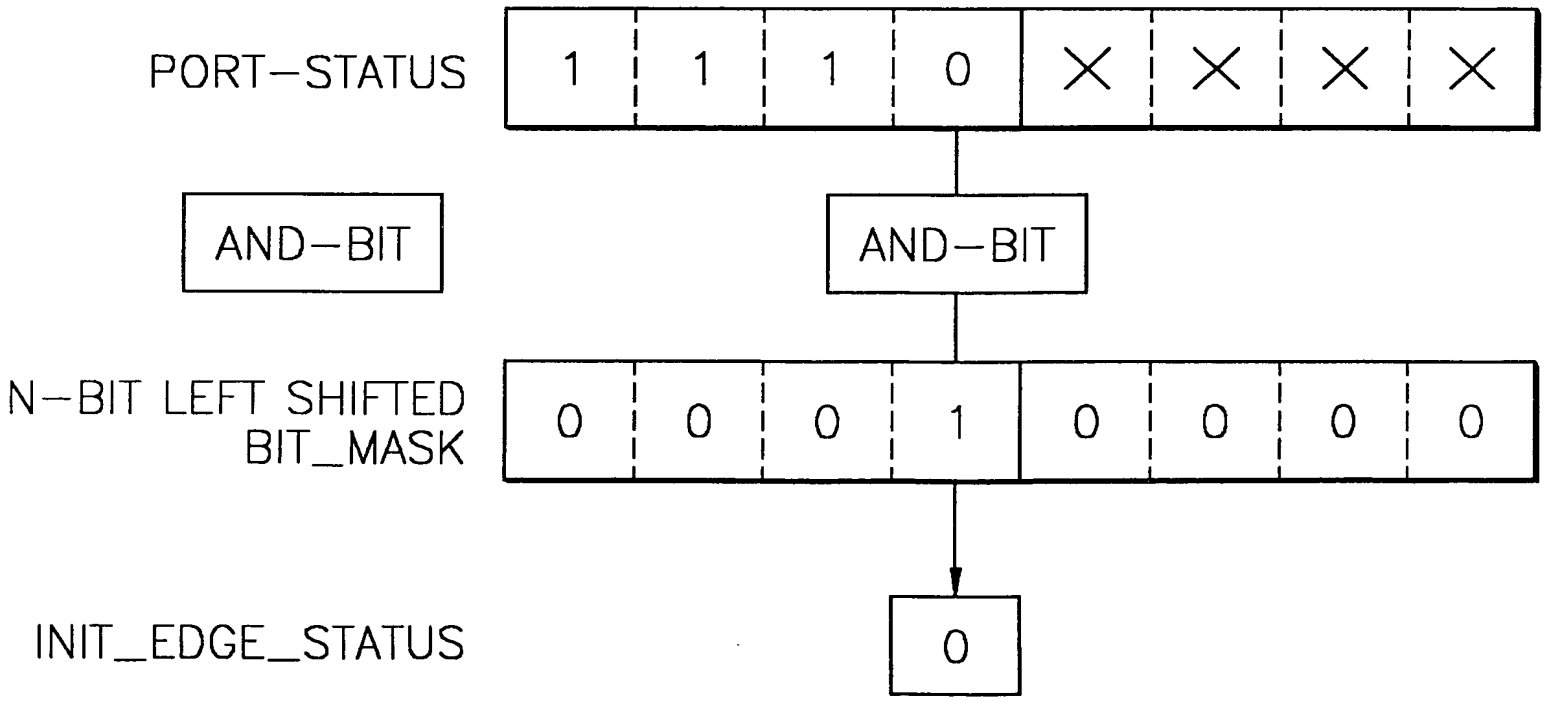

FIG. 16D

	X1	X2	X3	X4
Init_Edge_Status	0	1	0	1
Time[0]	130	10	160	50
Time[1]	280	170	300	210
Time[2]	430	320	450	360

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FIG. 17

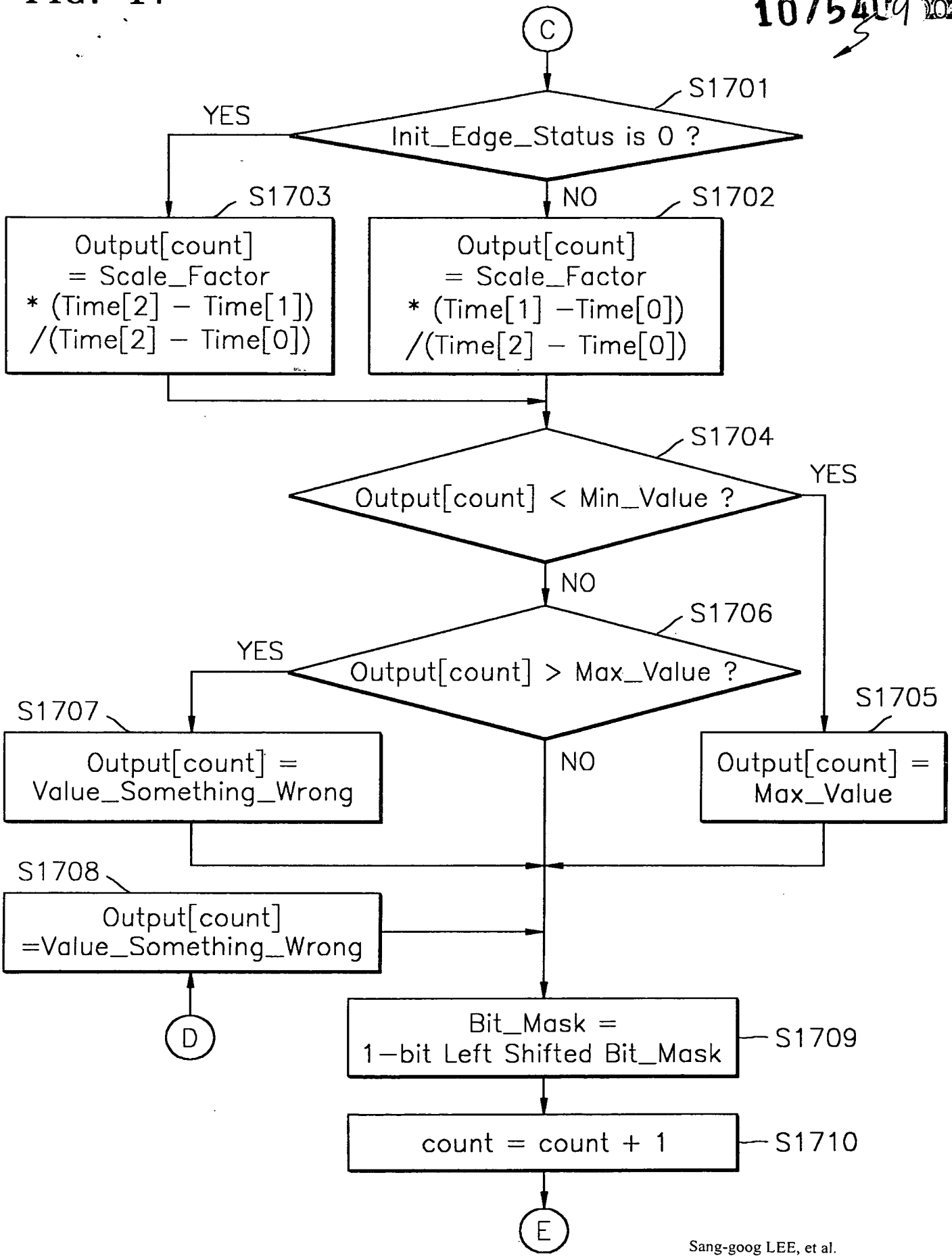
C
S1701
Init_Edge_Status is 0 ?
YES
NO
S1703
Output[count] = Scale_Factor * (Time[2] – Time[1]) /(Time[2] – Time[0])
S1702
Output[count] = Scale_Factor * (Time[1] –Time[0]) /(Time[2] – Time[0])
S1704
Output[count] < Min_Value ?
YES
NO
S1706
Output[count] > Max_Value ?
YES
NO
S1707
Output[count] = Value_Something_Wrong
S1705
Output[count] = Max_Value
S1708
Output[count] =Value_Something_Wrong
D
Bit_Mask = 1–bit Left Shifted Bit_Mask
S1709
count = count + 1
S1710
E

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FIG. 18A

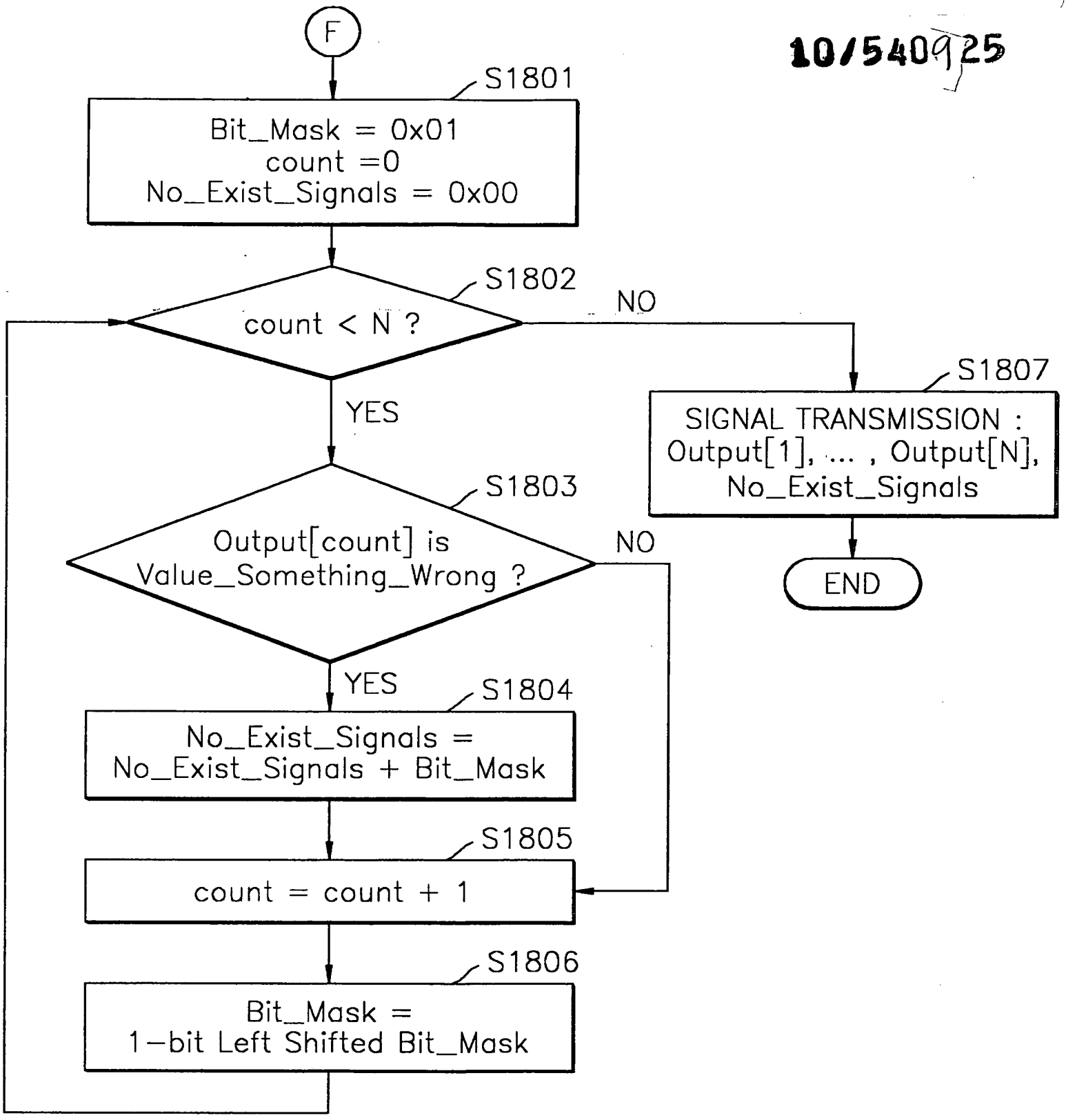

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FIG. 18B

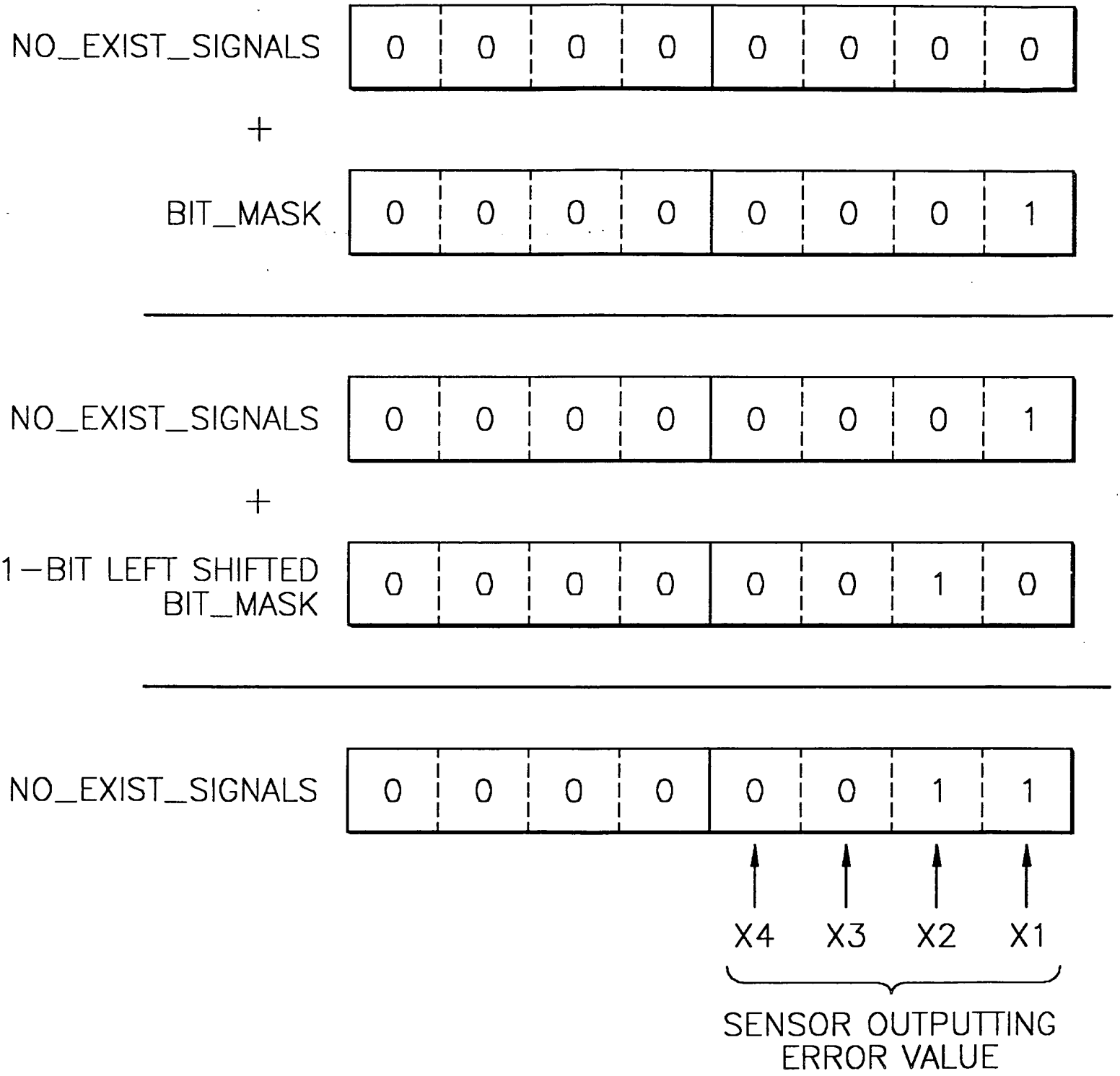

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FIG. 19

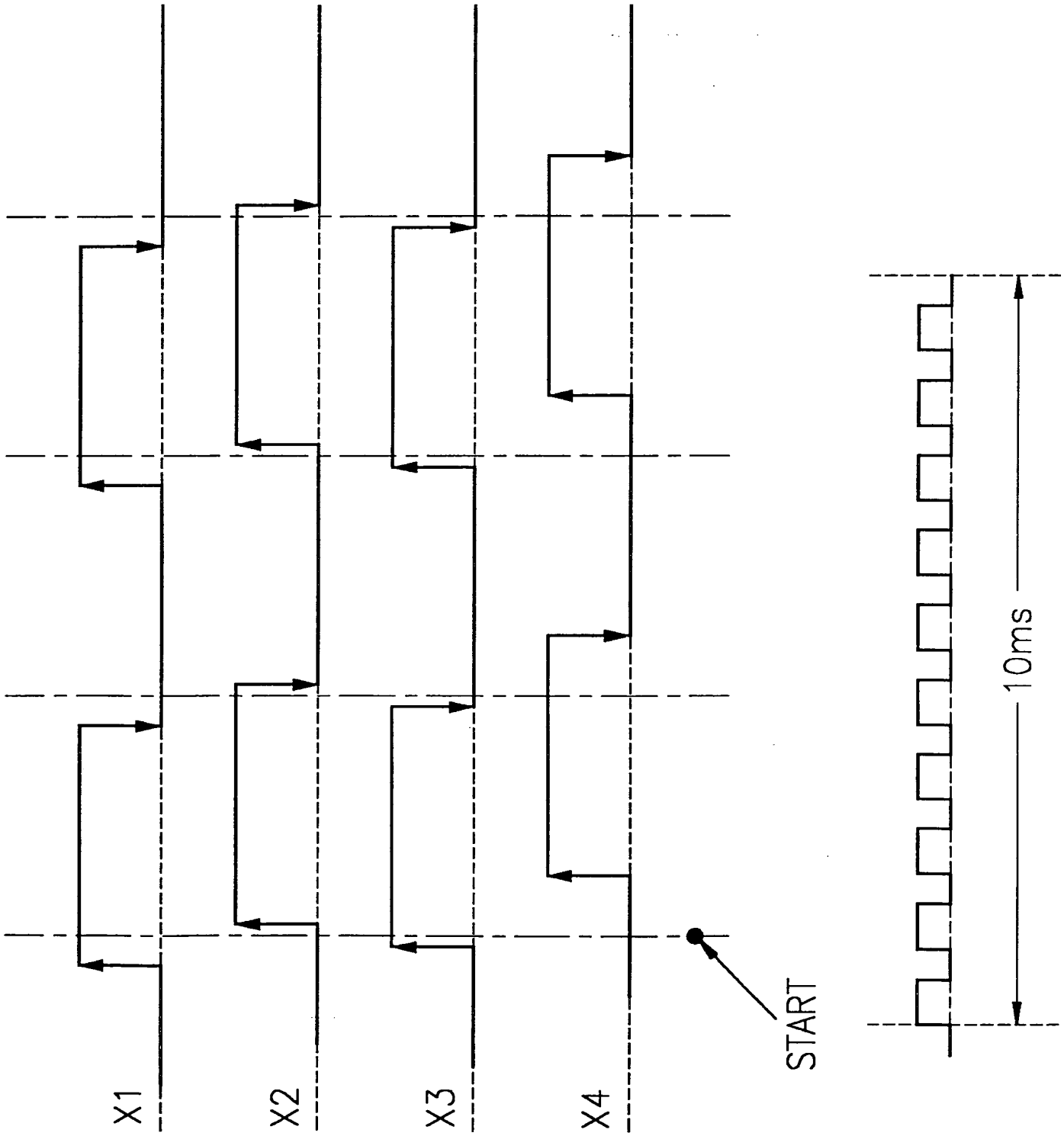

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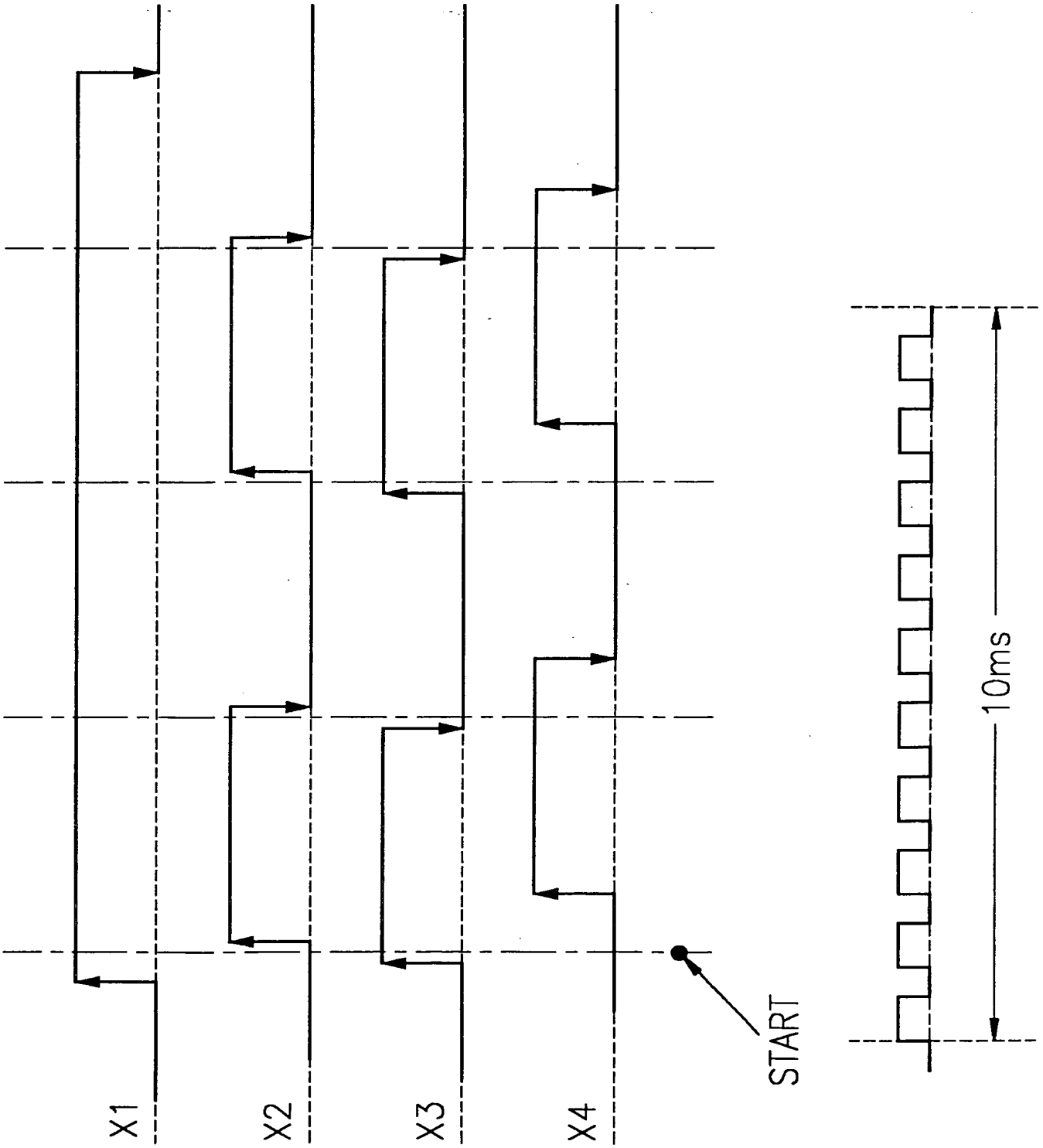
FIG. 20



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